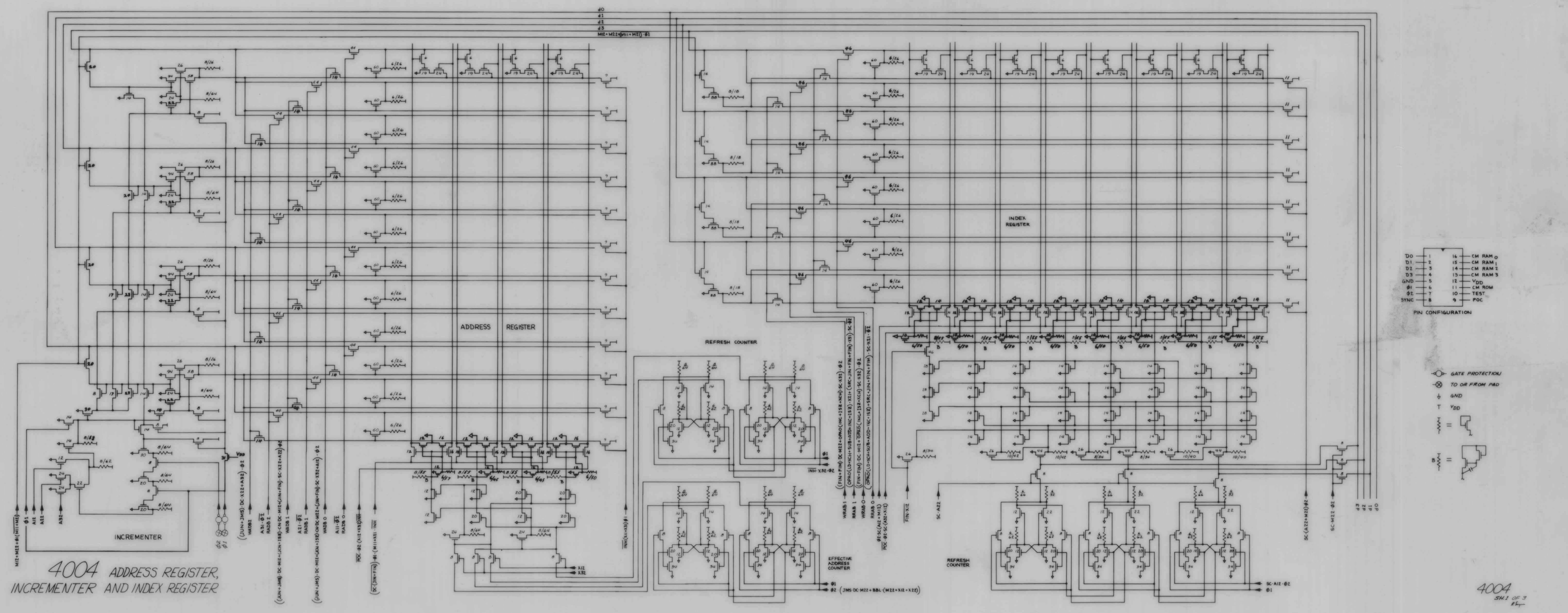
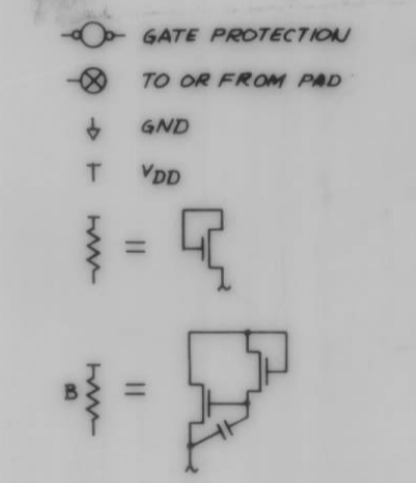


REV	DESCRIPTION	DRAWN	DATE



D0 - 16 - CM RAM 0  
 D1 - 15 - CM RAM 1  
 D2 - 14 - CM RAM 2  
 D3 - 13 - CM RAM 3  
 GND - 12 - VDD  
 #1 - 11 - CM ROM  
 #2 - 10 - TEST  
 SYNC - 9 - POC

PIN CONFIGURATION



4004 ADDRESS REGISTER, INCREMENTER AND INDEX REGISTER

4004 SHEET 1 OF 3

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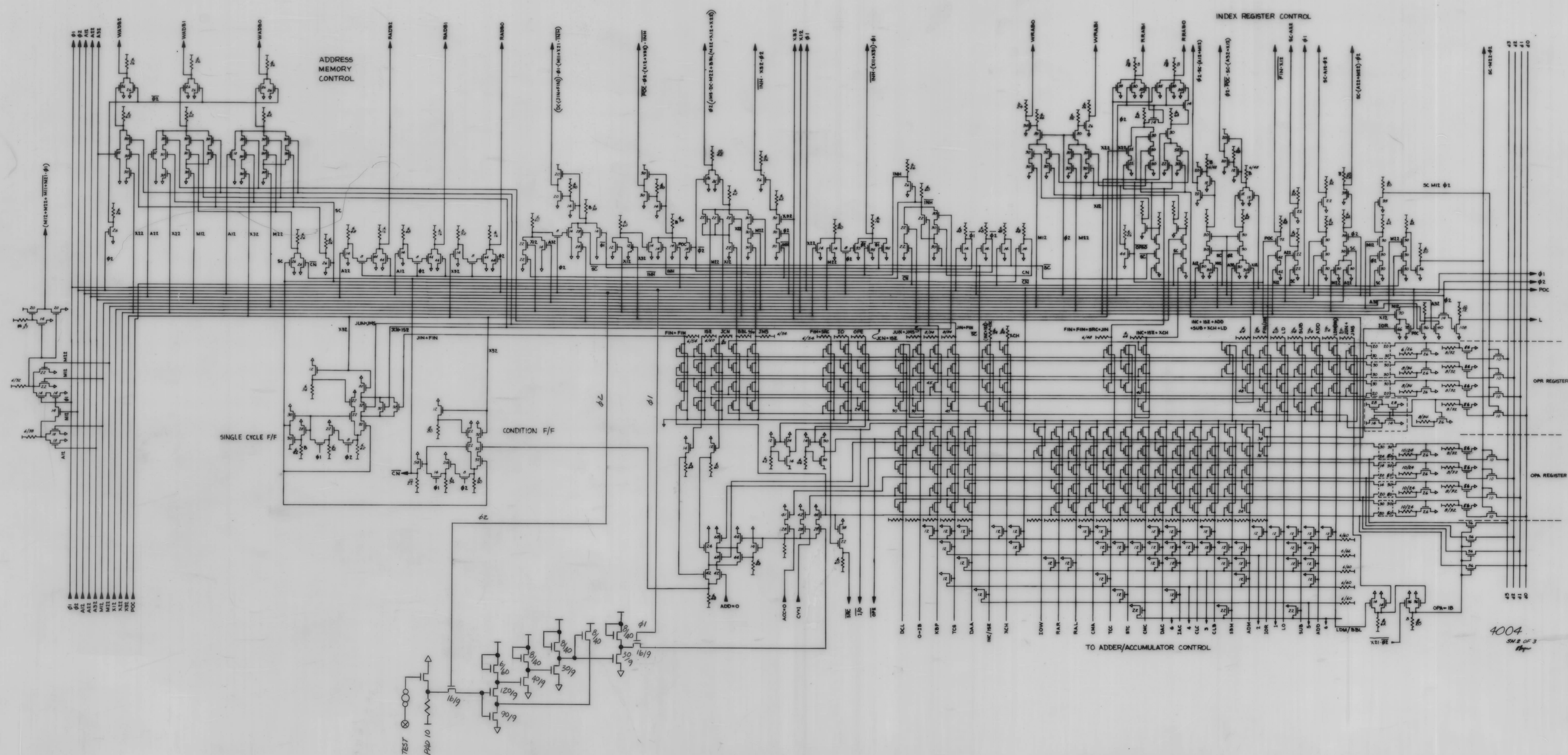
intel CORP. 3065 BOWEN AVE. SANTA CLARA CALIF. 95051

**4004 ADDRESS REGISTER, INCREMENTER AND INDEX**

DRW		SIZE	CODE	DRAWING NO.
CND		SCALE	REV	SHT. 1 OF 3
APPR				



REV	DESCRIPTION	DRAWN DATE
G	ADDED CIRCUIT TO TEST INPUT	GUTHERS - 8-676



4004  
SH 2 OF 3

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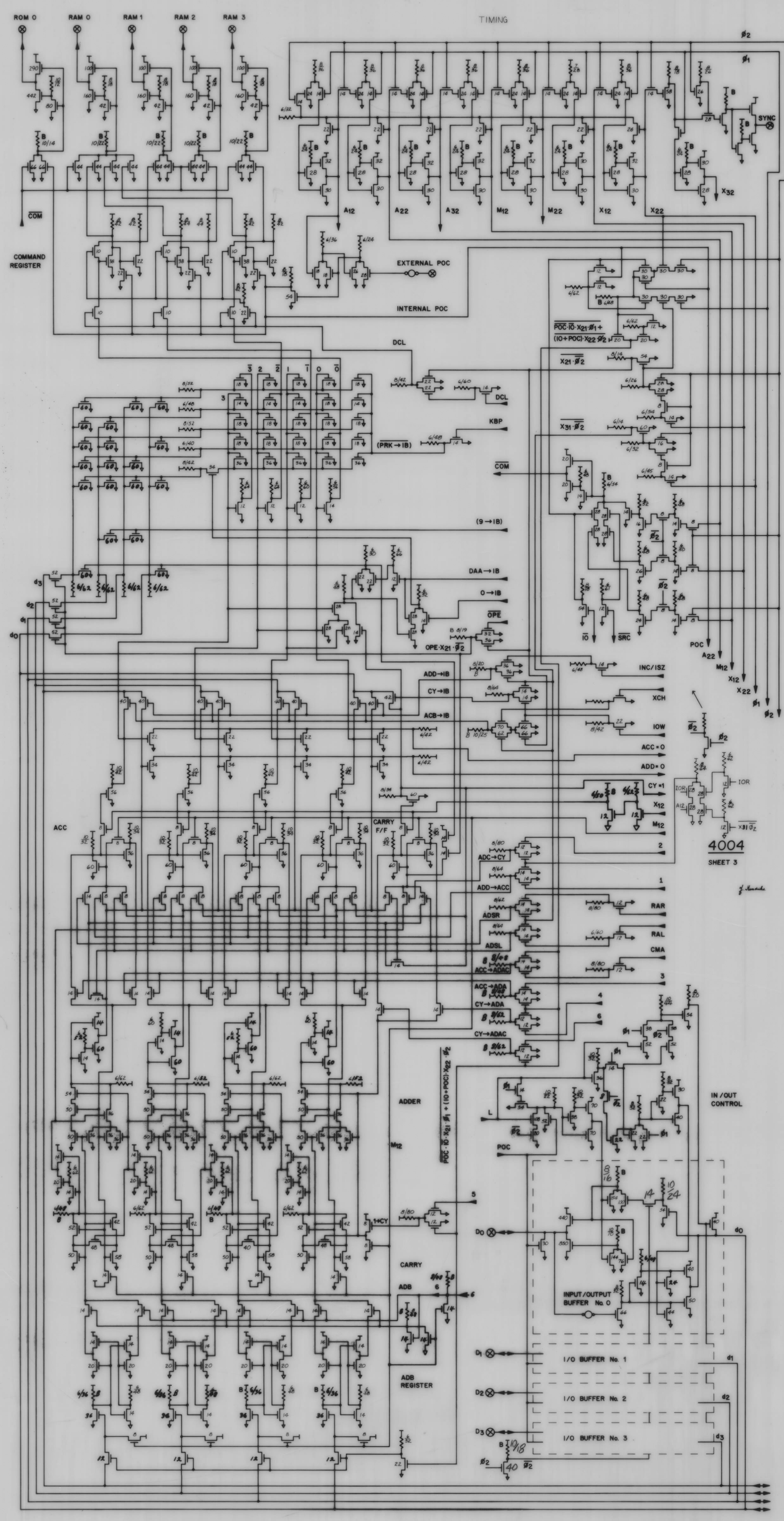
4004

DRW		SIZE CODE	DRAWING NO
CID		SCALE	REV
APPR			

INT. 2 OF 3



REV	DESCRIPTION	DRAWN	DATE
G	DEVICE SIZE CHANGES IN I/O BUFFER TO FIX TO H PROBLEM	KUTIERREZ	8.6.76



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4004

DRW		SIZE	CODE	DRAWING NO.
CHKD		SCALE	REV	SHT. 3 OF 3
APPR				